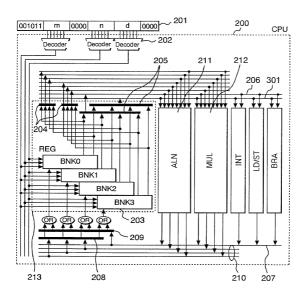
FIG. 1



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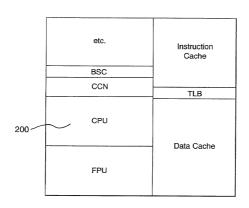
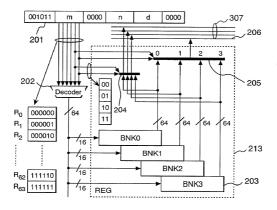


FIG. 3



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FIG. 4

а	b	С	d	е	f	R0	R1	R2	R3	R4	R5	R6	R7	R8	R59	R60	R61	R62	R63
0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0
0	0	0	0	1	1	0	0	0	1	1	1	1	0	0	 . 0	0	0	0	0
0	0	0	1	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0
0	0	0	1	0	1	0	0	0	0	0	1	1	1	1	0	0	0	0	0
0	0	0	1	1	0	0	0	0	0	0	0	1	1	1	0	0	0	0	0
		:								:							:		1
1	1	1	0	1	1	0	0	0	0	ò	0	0	0	0	1	1	i	1	0
1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1
1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	 . 0	0	1	1	1
1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1
1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1

FIG. 5

а	b	To 206	To 307				
0	0	XO	X1	X2	ХЗ		
0	1	X1	X2	ХЗ	X0		
1	0	X2	ХЗ	X0	X1		
1	1	хз	X0	X1	X2		

FIG. 6

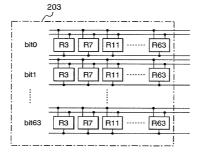


FIG. 7

а	b		input	t		output				
0	0	Х	Υ	z	0	Х	Υ	Z		
0	1	Х	Υ	Z	Z	0	Х	Υ		
1	0	Х	Υ	Z	Υ	z	0	Х		
1	1	Х	Υ	z	Х	Υ	Z	0		

FIG. 8

а	b	Х	output			
0	0	X	Х	0	0	0
0	1	Х	0	Х	0	0
1	0	Х	0	0	Χ	0
1	1	Х	0	0	0	Х

PACK.W Rm, Rn, Rd

	xxxxxx	m	xxxxx	n	d	0000
:	31 26	25 20	19 16	15 10	9 4	3 0

operation

source [1] ← SignExtend₆₄(Rm); source [2] ← SignExtend₆₄(Rm+1);

source [3] ← SignExtend₆₄(Rm+2);

source [4] \leftarrow SignExtend₆₄(Rm+3); amount ← ZeroExtend₆₄(Rn);

REPEAT i FROM 0 FOR 4

result [i] ← ZeroExtend₁₆(source[i]>>amount);

Rd ← MultiRegister₁₆(result);

FIG. 10

PACKI, W Rm. s. Rd

xxx	oox	m	s	d	0000
31	26 25	5 20	19 10	9 4	3 0

operation

source [1] ← SignExtend₆₄(Rm); source [2] ← SignExtend₆₄(Rm+1);

source [3] ← SignExtend₆₄(Rm+2);

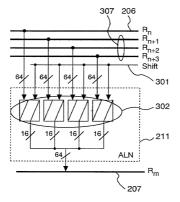
source [4] ← SignExtend₆₄(Rm+3); amount ← SignExtend₁₀(s);

REPEAT i FROM 0 FOR 4

result [i] ← ZeroExtend₁₆(source[i]>>amount);

Rd ← MultiRegister₁₆(result);

FIG. 11



UNPACK.W Rm, Rd

xxxx	xx		m	xx	xxx	00	0000	d		000	0
31	26	25	20	19	16	15	10	9	4	3	0

operation

source ← SignExtend₆₄(Rm); REPEAT I FROM 0 FOR 4

result [i] ← SignExtend₆₄(SignExtend₁₆ (source[i]));

Rd ← Register(result [0]); Rd+1 ← Register(result [1]);

Rd+2 ← Register(result [2]);

Rd+3 ← Register(result [3]);

FIG. 13

UNPACK.B Rm. Rw. Rd

	xxxxxx	m	xx	xxx	w		d	0000	
;	31 26	25	20 19	16	15	10	9 4	3	0

operation

source ← SignExtende/(Rm);

REPEAT i FROM 0 FOR 8

result [i] ← SignExtend₆₄(SignExtend₈ (source[i]));

Rw ← Register(result [0]);

Rw+1 ← Register(result [1]); Rw+2 ← Register(result [2]);

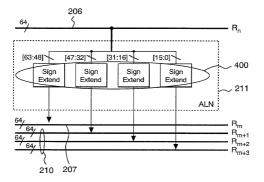
Rw+3 ← Register(result [3]);

Rd ← Register(result [4]);

Rd+1 ← Register(result [5]);

Rd+2 ← Register(result [6]); Rd+3 ← Register(result [7]);

FIG. 14



MSHFLE.W Rm, Rn, Rd 0000 XXXXXX xxxxx 26 25 2019 1615 10 9 43 source1 ← MultiZeroExtend₁₆(Rm); operation source2 ← MultiZeroExtend₁₆(Rm+1); source3 ← MultiZeroExtend₁₆(Rn); source4 ← MultiZeroExtend₁₆(Rn+1); REPEAT i FROM 0 FOR 2 result1[i x2] \leftarrow source2[i+2]; result1[(i x2)+1] \leftarrow source1[i+2]; result2[i x2] ← source2[i]; result2[(i x2)+1] ← source1[i]; result3[i x2] \leftarrow source4[i+2]; result3[(i x2)+1] \leftarrow source3[i+2]; result4[i x2] ← source4[i]; result4[(i x2)+1] ← source3[i]; Rd ← MultiRegister₁₆(result1); Rd+1 ← MultiRegister₁₆(result2); Rd+2 ← MultiRegister₁₆(result3); Rd+3 ← MultiRegister₁₆(result4);

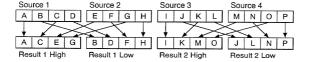
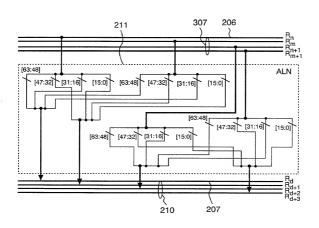


FIG. 17



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MSHFHI.W R	m, Rn, Rd	xxxxxx	m	xxxxx	n	d	0000	
		31 26	25 20	19 16	15 10	9 4	3 0	i
operation	source1 + 1 source2 + 1 source3 + 1 source5 + 1 source6 + 1 source6 + 1 source6 + 1 fresult1[i x2] result3[i x2] result4[i x2] Rd + MultiFRd+1 + Mu Rd+2 + Mu Rd+3 + Mu Rd+3 + Mu	MultiZen MultiZen MultiZen MultiZen MultiZen MultiZen MultiZen MultiZen MultiZen ← sourc ← sourc ← sourc ← sourc thitRegist titRegist	DExtendo DE DESTRUCTURA DE DE DESTRUCTURA DE DESTRUCTURA DE DESTRUCTURA	result result 1); ult2); ult3);	n+1); n+2); n+3); l; +1); +2); +3); :1[(i x2)+ :2[(i x2)+ :3[(i x2)+	-1] ← so -1] ← so	urce3 urce5	[i+2]; [i+2];

MSHFLO.W Rm, Rn, Rd XXXXXX XXXXX 0000 31 26 25 2019 1615 109 43 operation source1 ← MultiZeroExtend₁₆(Rm); source2 ← MultiZeroExtend₁₆(Rm+1); source3 ← MultiZeroExtend₁₆(Rm+2); source4 ← MultiZeroExtend₁₆(Rm+3); source5 ← MultiZeroExtend₁₆(Rn); source6 ← MultiZeroExtend₁₆(Rn+1); source7 ← MultiZeroExtend₁₆(Rn+2); source8 ← MultiZeroExtend₁₆(Rn+3); REPEAT i FROM 0 FOR 2 result1[i x2] ← source2[i]; result1[(i x2)+1] ← source1[i]; result2[i x2] ← source4[i]; result2[(i x2)+1] ← source3[i]; result3[i x2] ← source6[i]; result3[(i x2)+1] ← source5[i]; result4[i x2] ← source8[i]; result4[(i x2)+1] ← source7[i]; Rd ← MultiRegister₁₆(result1); Rd+1 ← MultiRegister₁₆(result2); Rd+2 ← MultiRegister₁₆(result3); Rd+3 ← MultiRegister₁₆(result4);

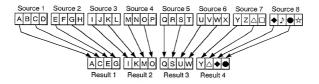
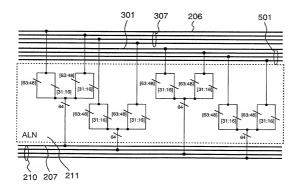


FIG. 21



MMAC.WQ Rm, Rn, Rw

	xxxxx	xx		m	xx	xxx		n	w		000	00
:	31	26	25	20	19	16	15	10	9	4	3	0

```
 \begin{array}{lll} \text{operation} & \text{source1} \leftarrow \text{MultiSignExtend}_{16}(\text{Rm}); \\ \text{source2} \leftarrow \text{MultiSignExtend}_{16}(\text{Rm+1}); \\ \text{result} [0] \leftarrow \text{SignExtend}_{64}(\text{Rn}); \\ \text{result} [1] \leftarrow \text{SignExtend}_{64}(\text{Rn+1}); \\ \text{result} [2] \leftarrow \text{SignExtend}_{64}(\text{Rn+2}); \\ \text{result} [3] \leftarrow \text{SignExtend}_{64}(\text{Rn+3}); \\ \text{REPEAT i} \text{FROM 0} \text{FOR 4} \\ \\ \\ \text{temp} \leftarrow \text{source1}[i] \times \text{source2}[i]; \\ \text{temp} \leftarrow \text{SignedSaturate}_{64}(\text{result} [i] + \text{temp}) \\ \\ \\ \text{Rw} \leftarrow \text{Register(result} [0]); \\ \text{Rw+1} \leftarrow \text{Register(result} [1]); \\ \text{Rw+2} \leftarrow \text{Register(result} [2]; \\ \text{Rw+3} \leftarrow \text{Register(result} [3]); \\ \end{array}
```

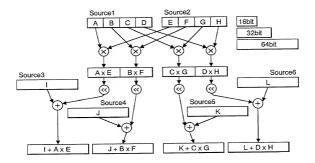


FIG. 24

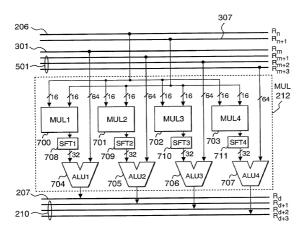


FIG. 25A FIG. 25B

mulu.l	r51, r24, r51	mulu.l	r51, r24, r51
mulu.l	r52, r24, r52	mulu.l	r52, r24, r52
mulu.l	r53, r24, r54	mulu.l	r53, r24, r54
mulu.l	r54, r24, r54	mulu.l	r54, r24, r54
shlri	r51, 9, r51	pack.w	r51, 9, r55
shlri	r52, 9, r52		
shlri	r53, 9, r53		
shiri	r54, 9, r54		
st.w	r17, 0, r51	st.q	r17, 0, r55
st.w	r17, 2, r52		
st.w	r17, 4, r53		
st.w	r17, 6, r54		

FIG. 26A FIG. 26B

mshfhi.w	r17, r63, r20
mshflo.w	r17, r63, r17
mshfhi.l	r20, r63, r19
mshflo.l	r20, r63, r20
mshfhi.l	r17, r63, r21
mshflo.l	r17, r63, r17

unpack r17, r17

FIG. 27A

FIG. 27B

mshfhi.w mshflo.w mshfhi.w mshflo.w mshfhi.w mshflo.w mshfhi.w	r21, r6, r31 r21, r6, r21 r31, r21, r41 r31, r21, r42 r22, r7, r32 r22, r7, r22 r32, r22, r43
mshflo.w	r22, r7, r22
mshflo.w	r32, r22, r43 r32, r22, r44

mshfle.w r21, r6, r41